

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	8	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency)	USPAT	OR	OFF	2008/02/27 20:13
S2	14	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/27 20:14

		same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency)				
S3	14	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/27 20:18
S4	4	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/27 20:19

		circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 same phase same differen\$6				
S5	4	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 same phase\$6 same differen\$6	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/27 20:19

S6	2	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 same phase\$6 same differen\$6 and long\$6 same term\$6 same drift \$6	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/27 20:22
S7	3	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to	USPAT	OR	OFF	2008/02/27 20:36

		same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 same phase\$6 same differen\$6				
S8	7	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 and phase\$6 same differen\$6	USPAT	OR	OFF	2008/02/27 20:37

S9	5	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 and phase\$6 same differen\$6 and (PLL or phase same lock \$6 same loop\$6)	USPAT	OR	OFF	2008/02/27 20:37
S10	3	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to	USPAT	OR	OFF	2008/02/27 20:38

		same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 and phase\$6 same differen\$6 and (PLL or phase same lock \$6 same loop\$6) and feedback\$6				
S11	2	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 and phase\$6 same differen\$6 and (PLL or phase same lock \$6 same loop\$6) and feedback\$6 and drift\$6	USPAT	OR	OFF	2008/02/27 20:38

S12	5	circuit\$6 and digital adj display \$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive \$6) and associat \$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 and phase\$6 same differen\$6 and (PLL or phase same lock \$6 same loop\$6)	USPAT	OR	OFF	2008/02/27 20:45
S13	4	circuit\$6 and digital adj display \$6 and generat\$6 same plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive \$6) and associat \$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6	USPAT	OR	OFF	2008/02/27 20:46



		or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 and phase\$6 same differen\$6 and (PLL or phase same lock \$6 same loop\$6)				
S14	3	circuit\$6 and digital adj display \$6 and generat\$6 same plural\$6 same pixel\$1 same data and (analog same image\$6 same data same receive \$6) and associat \$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 and phase\$6 same differen\$6 and (PLL or phase same lock \$6 same loop\$6)	USPAT	OR	OFF	2008/02/27 20:46

S15	3	circuit\$6 and digital adj display \$6 and generat\$6 same plural\$6 same pixel\$1 same data and (analog same image\$6 same data same receive \$6) and associat \$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 and phase\$6 same differen\$6 and (PLL or phase same lock \$6 same loop\$6)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/27 21:11
S16	2	circuit\$6 and digital adj display \$6 and generat\$6 same plural\$6 same pixel\$1 same data and (analog same image\$6 same data same receive \$6) and associat \$6 and (receiv\$6 same time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6	USPAT	OR	OFF	2008/02/27 21:12

		or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 and phase\$6 same differen\$6 and (PLL or phase same lock \$6 same loop\$6)				
S17	2	circuit\$6 and digital adj display \$6 and generat\$6 same plural\$6 same pixel\$1 same data and (analog same image\$6 same data same receive \$6) and associat \$6 and (receiv\$6 same time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 and phase\$6 same differen\$6 and (PLL or phase same lock \$6 same loop\$6)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/27 21:13
S18	0	"11408528" and "11408669"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/27 21:14

S19	0	"11408528"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/27 21:14
S20	0	"11408528"	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/27 21:15

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